Roll No ....

IT-503(B)-CBGS
B.Tech., V Semester
Examination, December 2020
Choice Based Grading System (CBGS)
Microprocessor and Interfacing
Time : Three Hours

Maximum Marks : 70

Note: i) Attempt any five questions.

ii) Each questions carry equal marks.

iii) All parts of each question to be attempted at one place.

iv) In case of any doubt or dispute the English version question should be treated as final.

1. a) Draw and discuss the internal block diagram of 8086. 7
   8086

   b) Discuss the Addressing modes of 8086. 7
   8086   Addressing modes

2. a) What is the use of memory interface? Give memory module design. 7
   Memory Interface

b) Explain the Physical memory organisation in an 8086 System. 7

8086

3. a) Draw and explain the architecture of 80286 processor.7

80286 processor

b) What are different types of interrupt supported by 8086 and explain ? 7

8086

4. a) Explain bit set reset mode of 8255 with applications. 7

8255

b) List and describe the working of various calls and return instruction in 8085 microprocessor. 7

8085 microprocessor'| return instruction

5. a) Explain the features of Pentium Processor. 7

Pentium Processor

b) What are the features of the SPARC architecture? 7

SPARC architecture

6. a) Explain how eight DIP switches are interfaced with 8085 microprocessor using a decoder. 7

microprocessor

b) Explain pipeline stages on Pentium processor. 7

Pentium processor

7. a) Draw and explain the block diagram of 8255. Also, explain different operating modes of 8255.　　　7

8255

8255

b) Discuss projection mechanism of 80386DX.　　7

80386DX

8. Explain the following terms with example :　　14
   a) AIU
   b) 80846 Microprocessor
   c) AGP
   d) PROM

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